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- 3 and 16**

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	In
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4527251 A	19850702	48	Remap method and apparatus for a m	714/8	711/202		Nibby, Jr., Cl
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5715419 A	19980203	17	Data communications system with add	711/212			Szczepanek;
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050125574 A1	20050609	13	Remapping routing information entries	710/36			Foster, Josep
	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6769046 B2	20040727	13	System-resource router	710/316	710/38		Adams, Lyle
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6229789 B1	20010508	28	Method and apparatus for traffic shap	370/230	398/9		Graves, Ala
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5956743 A	19990921	17	Transparent management of host interf	711/103	714/773		Bruce, Ricar
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5721931 A	19980224	37	Multiprocessing system employing an	710/260	710/312		Gephardt, D
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6594718 B1	20030715	12	Arbitration scheme for equitable distrib	710/240	710/107		Ebner, Sharc
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20010043602 A1	20011122	52	Method and apparatus for an increme	370/392	370/408		Brown, Davi
10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050117529 A1	20050602	25	Method and system for sending conne	370/282	370/229		Ramos-Escal
11	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6215748 B1	20010410	18	Data storage with data masking array,	369/53.31	369/275.3		Greenwood
12	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050066100 A1	20050324	15	System having storage subsystems and	710/300			Elliot, Rober
13	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050139261 A1	20050623	17	Managing transmissions between devi	710/316			Marushak, A
14	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6052773 A	20000418	46	DPGA-coupled microprocessors	712/43	712/229		DeHon, And

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